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PATENT
U.S. 10/715,950

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John L. Melanson
Serial No.: 10/715,950
Filed: November 11, 2003
Title: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM
SWITCHED MODE CIRCUITS
Confirm No.: 4450
Art Unit: 2683
Examiner: Stephen M. D'Agosta

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sirs:

RESPONSE TO FINAL OFFICE ACTION

In response to the Official Action mailed April 28, 2005, Applicant respectfully requests that the Examiner reconsider the rejection of the claims in view of the remarks set forth below.

Amendments to the Claims are reflected in the listing of claims, which begins on Page 2 of this paper.

Remarks/ Arguments begin on Page 10 of this paper.

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